# introspect technology

#### LP5-315 Interposer Characterization with RSH2

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### Interposer Construction & Model

#### **PHYSICAL CONSTRUCTION**



#### **THREE-PORT NETWORK MODEL**



CPU Side

\* Note that Port 3 includes the RSH S21 performance. The interposer has an integrated high-impedance tip for the RSH



### Test Setup





### Test Setup

RSH amplifier that goes to the oscilloscope or to the analyzer



Passive probe connected to TDR

This provides the stimulus input into one of the pads of the interposer

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Sense tap of the interposer connected to the LPDDR5 RSH amplifier

### Return Loss at Signal Pad

#### **T11 (TIME DOMAIN)**

#### **S11 (FREQUENCY DOMAIN)**





The -30 dB performance means that any reflection seen in the signal will not be due to the interposer



## Output 8.4 Gbps Eye (300 mV Input)



This is the output at Port 3 when excited by a live pattern

Note that this bandwidth is due to the legacy RSH amplifier bandwidth

